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APPLICANTS

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** CONTINUING DATA *****

This application is a 371 of PCT/JP03/09818 08/01/2003 *hfe*

** FOREIGN APPLICATIONS *****

JAPAN 2002-233708 08/09/2002 *hfe*

Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY JAPAN	SHEETS DRAWING 7	TOTAL CLAIMS 6	INDEPENDENT CLAIMS 3
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after allowance	EXAMINER'S SIGNATURE <i>Michael J. Fodor</i>	INITIALS <i>hfe</i>		
Verified and Acknowledged				

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TITLE

Method for manufacturing compound semiconductor wafer and compound semiconductor device

FILING FEE RECEIVED 1260	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue)
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